

## Hitex Technology Spotlight Choice of 3 Technologies



### Choice of 3 technologies i.MX6UL, STM32MP13x or i.MX91/93

by Paul Roberts.

#### Introduction

Normally, once a CPU from a particular vendor has been selected for your project, to change to an alternative would result in a redesign of your board. We explain why that doesn't always have to be the case.

#### Diverse and standard formats

Many vendors will have their own proprietary formats when it comes to module design around a CPU. Additionally, these tend to be different for every CPU that they implement. Alternatively, there are common formats such as SMARC and COMExpress for instance, where there is a standardised format that you can design to providing you with a choice of vendors. The Issue with these formats, however, is that they don't always expose all of the features that might be available from the CPU.

phyCORE®-i.MX 91/93  
phyCORE®-STM32MP13x  
phyCORE®-i.MX 6UL/ULL

Pin compatible

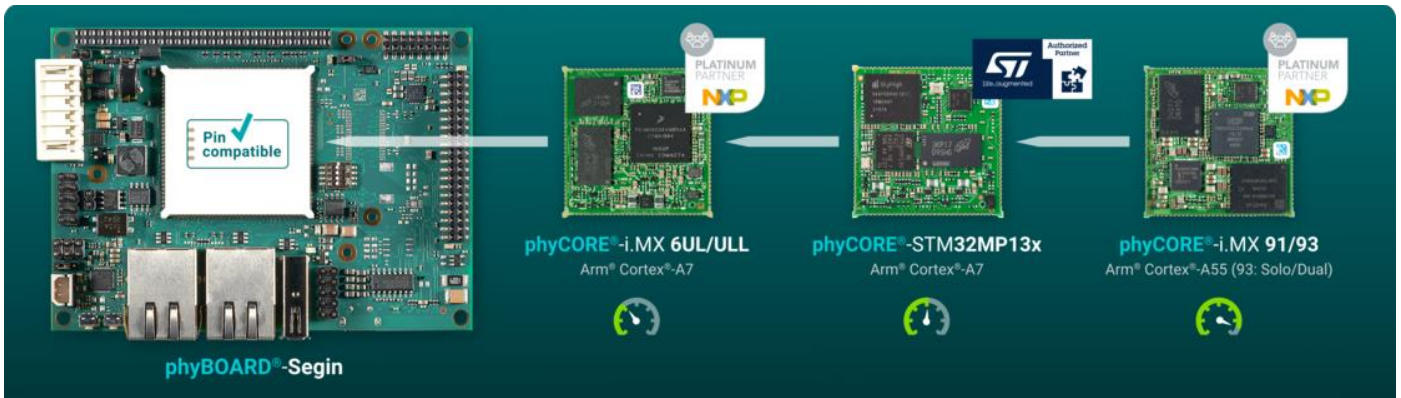
1 - 1,7 GHz

phyBOARD®-Segin

PHYTEC

## Flexible format

We have a number of vendors that buck this trend. One of them is Phytex, who have designed three of their modules into the same footprint to allow you the flexibility to use any of them in your design. This has a number of benefits such as allowing you to update your project specification during development to increase performance or to reduce power consumption or provide support for product variants with a single carrier board design. Other benefits might include obsolescence management or mitigation of sourcing or supply chain issues.



## Further Information

To read more about the [PhyBoard-Segin](#)

To read more about the individual modules look at

[PhyCORE-iMX6UL](#)

[PhyCORE-STM32MP13x](#)

[PhyCORE-i.MX91](#)

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